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APPLICATION NO.		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
09/749,661		12/28/2000	Jed D. Griffin	2207/10491	8716		
	23838 7	590 08/29/2003					
	KENYON &			EXAMINER			
	VASHINGTO	ET, N.W., SUITE 700 N, DC 20005		NGUYEN, PATRICIA T			
				ART UNIT	PAPER NUMBER		
				2817			
				DATE MAILED: 08/29/2003			

Please find below and/or attached an Office communication concerning this application or proceeding.

,		Application No.		Applicant(s)					
	09/749,661			GRIFFIN, JED D.					
	Examiner			Art Unit					
		Patricia T. N	-		2817				
Period fo	The MAILING DATE of this communication app r Reply	pears on the o	cover	sheet with the d	correspondence ad	ldress			
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). - Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). Status									
1)	Responsive to communication(s) filed on	 ·							
2a) <u></u>	This action is FINAL . 2b)⊠ Th	is action is n	on-fin	al.					
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.									
Dispositi	on of Claims	Ex parte Qui	ayı c ,	1900 C.D. 11, •	+33 O.G. 213.				
4)🖂	Claim(s) 1-15 is/are pending in the application	1.							
	4a) Of the above claim(s) is/are withdray	wn from cons	sidera	tion.					
5) Claim(s) is/are allowed.									
6)⊠	6)⊠ Claim(s) <u>1,4,5,8,9,13 and 14</u> is/are rejected.								
7)🖾	Claim(s) 2,3,6,7,10-12 and 15 is/are objected t	to.							
8) Claim(s) are subject to restriction and/or election requirement. Application Papers									
9)🖾 -	9)⊠ The specification is objected to by the Examiner.								
10)🖾 🗆	10)⊠ The drawing(s) filed on <u>15 December 2000</u> is/are: a)□ accepted or b)⊠ objected to by the Examiner.								
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
11) 🔲 🗅	The proposed drawing correction filed on	_ is: a) <u> </u> ap _l	orove	d b)⊡ disappr	oved by the Examin	er.			
	If approved, corrected drawings are required in rep	ply to this Offic	ce acti	on.					
12) 🔲 🖯	12) The oath or declaration is objected to by the Examiner.								
Priority u	nder 35 U.S.C. §§ 119 and 120								
13)	13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).								
a)[a) All b) Some * c) None of:								
	1. Certified copies of the priority documents have been received.								
	2. Certified copies of the priority documents have been received in Application No								
* S	 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 								
14) 🗌 A	14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).								
	a) ☐ The translation of the foreign language provisional application has been received. 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.								
Attachment(s)									
1) Notice	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449) Paper No(s) €		5) 🔲		y (PTO-413) Paper No Patent Application (PT				

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DETAILED ACTION

Drawings

The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the N type load in claim 5, line 7; the first and second N type transistor in claim 9, lines 11 and 13; the P type and N type transistors in claim 12, lines 1 and 2 must be shown or the feature(s) canceled from the claim(s). No new matter should be entered. It appears that no drawing is corresponding to the connections of the circuit as claimed in the claims.

A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

Claim Objections

Claims 5, 9, 12 are objected to because of the following informalities:

The N type load in claim 5, line 7; the first and second N type transistor in claim 9, lines 11 and 13; the P type and N type transistors in claim 12, lines 1 and 2 have no support in the drawings or specification. It appears that no drawing or specification is corresponding to the connections of the circuit as claimed in the claims.

In claims 1, 5, 9 the N-type load and P-type load have been using the same terminology for different elements. For example, claim 1, lines 6 and 7, an N-type load P-type load are different loads than the ones in line 9.

Appropriate correction is required.

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Specification

The disclosure is objected to because of the following informalities:

It appears that the specification does not support the corresponding connections of the circuit as claimed in the claims 5, 9, and 12 such as the N type load in claim 5, line 7; the first and second N type transistor in claim 9, lines 11 and 13; the P type and N type transistors in claim 12, lines 1 and 2.

Appropriate correction is required.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 4-5, 8, 9, 13, 14 are rejected under 35 U.S.C. 102(b) as being anticipated by Shin et al., U.S. Patent # 5,703,532.

Fig. 2 of Shin et al. discloses a circuit comprising: For claim 1, transistors 22, 42 can be read as first and second P type input amplifiers wherein input A-in can be read as a non-inverted differential voltage signal, input B-in can be read as an inverted differential voltage signal; transistors 21, 41 can be read as first and second N type input amplifiers; transistors 27, 28 can be read a first output amplifier wherein transistor 24 can be read as an N type load and transistor 25 can be read as a P type load; transistors 47, 48 can be read as a second output amplifier wherein transistor 44 can be read as an N type load and transistor 45 can be read as a P type load.

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Regarding claims 4, 8, 14, see spec. col. 9, lines 4-40 and col. 8, lines 11-36.

Regarding claim 5, transistors 22, 42 can be read as first and second P type input amplifiers wherein input A-in can be read as a non-inverted differential voltage signal, input B-in can be read as an inverted differential voltage signal; transistors 21, 41 can be read as first and second N type input amplifiers; transistors 27, 28 can be read a first output amplifier wherein transistor 24 can be read as an N type load and transistor 26 can be read as an N type load; transistors 47, 48 can be read as a second output amplifier wherein transistor 44 can be read as an N type load and transistor 46 can be read as an N type load.

Regarding claim 9, transistors 22, 42 can be read as first and second P type input amplifiers wherein input A-in can be read as a non-inverted differential voltage signal, input B-in can be read as an inverted differential voltage signal; transistors 21, 41 can be read as first and second N type input amplifiers; transistors 27, 28 can be read a first output amplifier; transistors 24, 44 can be read as a first N type load and a second N type load; transistors 25, 45 can be read as a first P type load and a second P type load; transistor 23 can be read as a first intermediate transistor; transistors 47, 48 can be read as a second output amplifier wherein transistor 44 can be read as an N type load and transistor 46 can be read as an N type load.

Regarding claim 13, see spec. col. 1, lines 20-27.

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Allowable Subject Matter

Claims 2, 3, 6, 7, 10, 11, 12, 15 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. U.S. patents # 6,429,735 of Kuo et al., # 4,377,789 of Hoover, # 3,991,380 of Pryor contain some limitations of the claimed inventionAny inquiry concerning this communication or earlier communications from the examiner should be directed to Patricia T. Nguyen whose telephone number is (703) 308-1927. The examiner can normally be reached on 6:30 AM - 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (703) 308-4909. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

PTN August 25, 2003

PATRICIA NGUYEN PRIMARY EXAMINER

Patricia reguizer